

OCT 04 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.10/635,715
Confirmation No.2007
Filing DateAugust 5, 2003
InventorW. G. Scott
Assignee Micron Technology, Inc.
Group Art Unit2812
Examiner Richard A. Booth
Attorney's Docket No. MI22-2375
Customer No. 021567
Title: Methods of Forming Transistor Gates

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR § 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at 571-273-8300 on the date shown below:

1. Certificate of Facsimile Transmission
2. Response to August 15, 2005 FINAL Office Action

Dated: October 4, 2005By: 

Jennifer Gaines, Legal Assistant
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FEE DEFICIENCY

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: October 4, 2005By: 

David G. Latwesen, Ph.D.
Reg. No. 38,533

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RESPONSE TO AUGUST 15, 2005 FINAL OFFICE ACTION

Response to Final Office Action

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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AMENDMENTS